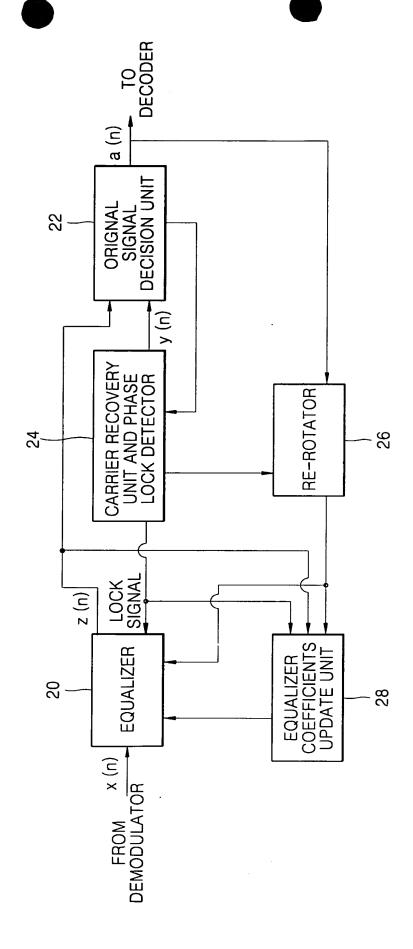


FIG. 2



244 242 247 a (n) SIGNAL DECESION UNIT PHASE LOCK DETECTOR DETECTOR 00 P ORIGINAL PHASE ERROR 22 26 RE-ROTATOR (u)9(u) 8 <u>S</u> 248 CONJUGATE COMPLEX NUMBER GENERATOR 246 COUNTER 262 249 LOCK SIGNAL 24 a (n) e (n) a (n) 264 208 z (n) FEED BACK EQUALIZER a (n) e ^{je(n)} i SECOND COEFFICIENTS UPDATER 286 (n) z 204 20 GENERATOR 206 FIRST COEFFICIENTS UPDATER FEED FORWARD EQUALIZER 202 282-28 284 E ×

FIG. 3